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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
09/902,051	07/10/2001	Mark J. Chambers	TI-30883	7744	
23494	7590 05/24/2006		EXAMINER		
TEXAS INSTRUMENTS INCORPORATED			DAVIDSON, DAN		
P O BOX 655474, M/S 3999 DALLAS, TX 75265			ART UNIT	PAPER NUMBER	
ŕ			2627		
				DATE MAILED: 05/24/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)				
Office Action Summary		09/902,051	CHAMBERS ET AL.				
		Examiner	Art Unit				
		Dan I. Davidson	2627				
Period fo	The MAILING DATE of this communication or Reply	n appears on the cover sheet w	vith the correspondence address	SS			
WHIC - Exter after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR R CHEVER IS LONGER, FROM THE MAILIN nsions of time may be available under the provisions of 37 C SIX (6) MONTHS from the mailing date of this communication of period for reply is specified above, the maximum statutory p re to reply within the set or extended period for reply will, by reply received by the Office later than three months after the ed patent term adjustment. See 37 CFR 1.704(b).	IG DATE OF THIS COMMUN FR 1.136(a). In no event, however, may a on. period will apply and will expire SIX (6) MO statute, cause the application to become A	ICATION. reply be timely filed NTHS from the mailing date of this commu				
Status							
1)	Responsive to communication(s) filed on	27 March 2006.					
· · · · · · · · · · · · · · · · · · ·		This action is non-final.					
′=	Since this application is in condition for all		tters, prosecution as to the me	erits is			
,	closed in accordance with the practice un	·	•				
Dispositi	on of Claims						
4)⊠	4)⊠ Claim(s) <u>1-10</u> is/are pending in the application.						
,	4a) Of the above claim(s) is/are withdrawn from consideration.						
5)[Claim(s) is/are allowed.						
6)⊠	☑ Claim(s) <u>1-10</u> is/are rejected.						
7)	Claim(s) is/are objected to.						
8)□	Claim(s) are subject to restriction a	and/or election requirement.					
Applicati	on Papers						
9)□	The specification is objected to by the Exa	miner					
	The drawing(s) filed on is/are: a)□		by the Examiner				
,	Applicant may not request that any objection to						
	Replacement drawing sheet(s) including the co		• •	121(d)			
11)	The oath or declaration is objected to by the						
Priority u	ınder 35 U.S.C. § 119						
_	Acknowledgment is made of a claim for for	reign priority under 35 U.S.C.	§ 119(a)-(d) or (f).				
a)[☐ All b)☐ Some * c)☐ None of:						
	1. Certified copies of the priority docur						
	2. Certified copies of the priority docur						
	3. Copies of the certified copies of the		received in this National Stag	ge			
• •	application from the International Bu						
· S	see the attached detailed Office action for a	a list of the certified copies not	: received.				
Attachmeni ⇔⊠ N-#-	• •	_	_				
	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948	4) Linterview Paper No.	Summary (PTO-413) (s)/Mail Date				
3) 🔲 Inform	nation Disclosure Statement(s) (PTO-1449 or PTO/S r No(s)/Mail Date		Informal Patent Application (PTO-152	2)			

Application/Control Number: 09/902,051 Page 2

Art Unit: 2627

DETAILED ACTION

1. The amendment filed March 27, 2006 has been received and has been made of record. An Office Action in response to the above amendment follows.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 1-2 and 6-7 are rejected under 35 U.S.C. 102(b) as being anticipated by Du et al (US 5,898,532 A).

Du et al disclose an offset correction circuit to correct DC offset (Fig. 3, 250, 210; col. 4, lines 22-30; see Fig. 2 for DC offset generated by thermal asperity) in accordance with a data rate (col. 5, lines 39-48), comprising: a detection circuit to detect a thermal asperity signal (Fig. 3, 130, 230); and a filter circuit to respond to the thermal asperity signal tracking the data rate (Fig. 3, 210; col. 5, lines 39-48). Du et al further disclose a head to read/write information on the disk (Fig. 3, 120); a preamplifier to amplify the information (Fig. 3, 130); and a read channel to process the amplified information (Fig. 3, boxed-in area).

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and

Application/Control Number: 09/902,051 Page 3

Art Unit: 2627

the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

5. Claims 3-5 and 8-10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Du et al (US 5,898,532 A) as applied to claims 1 and 6 above, and further in view of Patti et al (US 6,359,743 B1).

Du et al do not disclose that the filter circuit is a transconductance circuit that includes an FET to shunt current in accordance with the data rate (Du et al simply do not detail a circuit structure for the filter circuit; they only disclose that the filter circuit is a high-pass filter having variable cut-off lower frequencies and specially constructed to mitigate the effects of thermal asperity). Patti et al teach this limitation (Fig. 5; Fig. 7, input from 110 to 130; col. 5, line 65 – col. 6, line 2; col. 8, lines 24-26). It would have been obvious to one of ordinary skill in the art at the time of Applicant's invention to use the above teaching of Patti et al in Du et al given no unexpected results; motivation being design choice in constructing a high-pass filter circuit having variable cut-off lower frequencies as a function of data rate to counter the effects of thermal asperity.

Conclusion

- 6. The rejection in the previous Office Action rejecting claims 1-10 under 35 U.S.C. § 102(e) as being anticipated by Patti has been withdrawn since although Patti discloses that the variable cut-off frequencies of the thermal asperity filter circuit can be determined based on a consideration of data rates, there is no indication in Patti that the filter circuit tracks the data rate in deciding which variable cut-off frequency to apply.
- 7. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP

Art Unit: 2627

§ 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dan I. Davidson whose telephone number is (571) 272-7552. The examiner can normally be reached on Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Andrea L. Wellington, can be reached on (571) 272-4483. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

Art Unit: 2627

you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

DID Dan I Davidson May 16, 2006

ANDREA WELLINGTON

SUPERVISORY PATENT EXAMINER